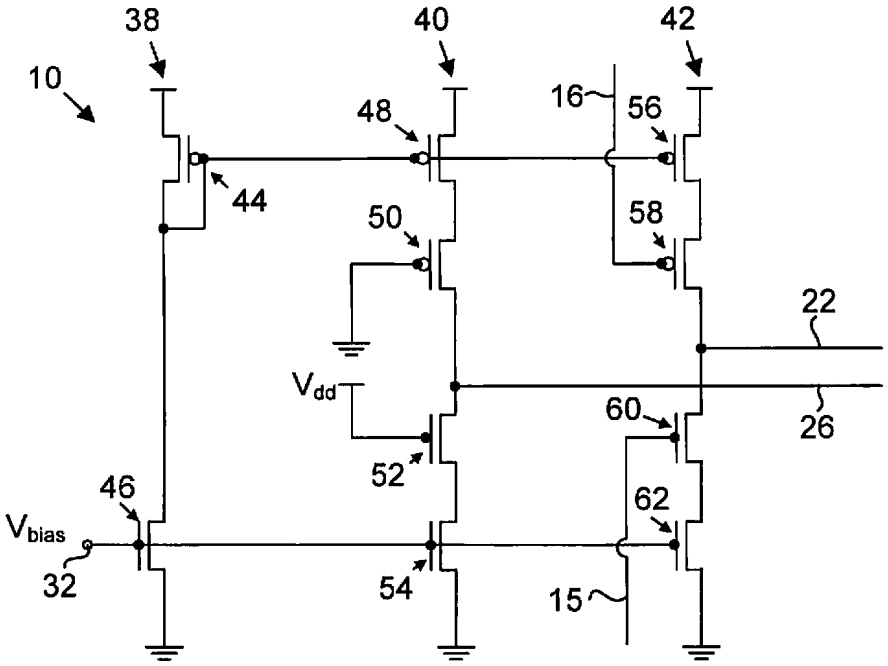
38
40
42
10
48
16
56
44
50
58
22
V_{dd}
60
26
52
46
62
V_{bias}
32
54
15

FIG. 2

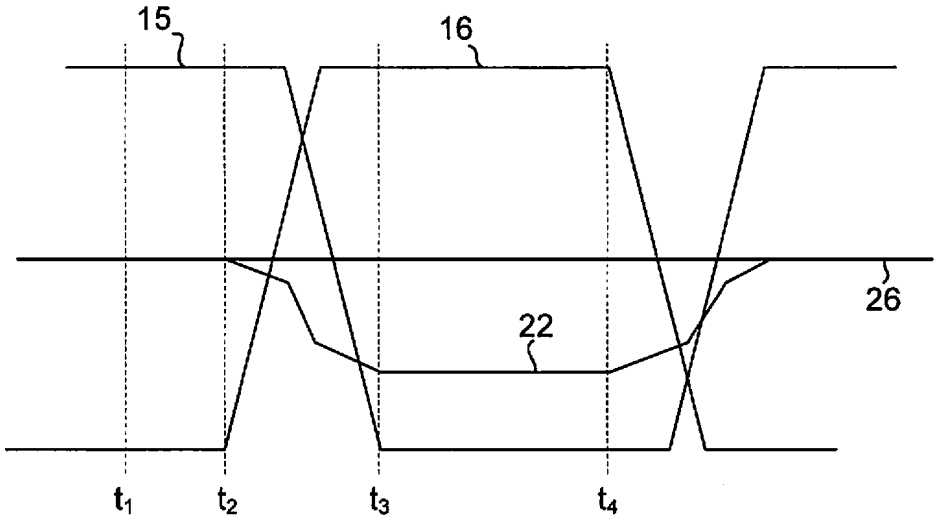
15
16
26
22
t_1
t_2
t_3
t_4

FIG. 3

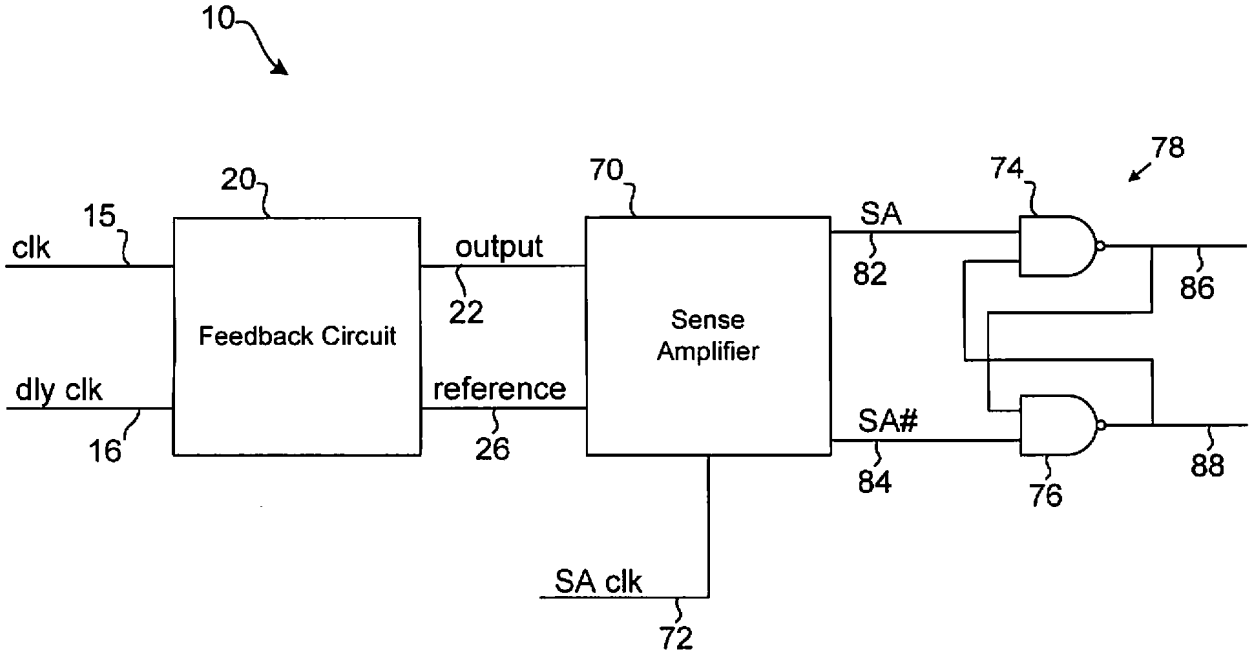
10
20
15
clk
dly clk
16
Feedback Circuit
output
22
reference
26
70
Sense
Amplifier
SA clk
72
SA
82
SA#
84
74
78
76
86
88

FIG. 4